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FALCON ID # 35160

IT / Hardware Engineer

Residential Country : India Nationality : India
Resume Title : Hardware Engineer Notice Period : 1 Days

EDUCATION

Qualification	Institute / College /university	Year	Country
B E / B Tech	Gujarat University	2012	India

CAREER SUMMARY

Position	Employer	Country	From Month/ Year	To Month/ Year
Pcb And Hardware Designer	Reputed Company	India	12/2011	08/2014

ADDITIONAL CERTIFICATE AND TECHNICAL QUALIFICATION

Name Of The Course	Course Date	Valid Upto	Name Of Organisation
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Current Salary (Monthly In Usd): Not Mention Expected Salary (Monthly In Usd): Not Mention

Additional Skills :

Technical Skills

- Knowledge of the following:
 - o PCB layout design, signal integrity and schematic design in Orcad and Cadstar
 - o Circuit simulation in Orcad Pspice
 - o Lay out design of mixed signal , high speed and RF high frequency designs.
 - o Layout with 5 BGA packages in one board 3 BGA packages were of 1148 balls and other two of 255 balls More than 7000 connections in one board
 - o Knowledge of Layer stack up planning with variable control impedances (Multilayer Board up to 16 Layers)

- o Pre routing as well as post routing analysis of signal Integrity
- o Knowledge of DDR2, ADC, DACs etc routing
- o Knowledge of differential pair, Shielding, Memory, Bus and Auto routing
- o Knowledge of Board testing & development of Graphical user interface in Lab VIEW for different mission.
- Generating the following:
 - o Schematic Symbol and PCB footprint from the data sheet
 - o Gerber and IPC-D and cross check generated data
 - o DXF file cross check with mechanical team
 - o Gerber data cross check in GC preview
- Skills in maintaining schematic and PCB integrated library and also in the database
- Well versed with Digital Design, Analog Design, Mixed Signal, High speed and RF Layout
- Proficient in modifying the design guideline
- Standard known IPC 2221,ISRO PAX 301 etc.

IT Skills

Operating Systems	Windows 98, 2003 &XP
Programming Languages	C, Assembly, National Instruments LabVIEW 7.0
FPGA Technologies	Virtex2 (BGA-728), Virtex4 (BGA-1148), Virtex5
Hardware Architecture	8051, 8085 &8086

Additional Information :

- Nearly 8 years of experience in Research & Development and Design Engineering
- Associated with SAC (ISRO)as a senior PCB layout and high speed design Engineer
- Adept at managing Design Engineering Operations involving design & development, developing detailed drawing for fabrication, co-ordination with internal departments and tool room operations
- Conversant with tools such as Cadstar-7, 9, 12, Orcad-9.2, 16, 16.3, National Instruments LabVIEW 7.0
- Possess excellent communication, planning, analytical, detail orientation, leadership & team player skills

Core Competencies

Design Engineering	Client Communications
Planning & Execution	Research & Development